Serial No.: 10/634,746 Amdt. dated July 13, 2006

Reply to Office Action dated April 17,

2006

Amendments to the Specification

Please replace the paragraph beginning at page 2, line 26, and ending at page 3, line 5, with the

following amended paragraph.

FIG. 2 is a block diagram illustrating an example of a conventional turbo decoder

apparatus, and in particular, illustrates an example of a detailed structure of the turbo decoder

block 48 shown in FIG. 1. In the drawing, the turbo decoder is constructed with, for example, a

Soft-In Soft-Out (SISO) scheme. The turbo decoder can also be implemented using a (MAP)

Maximum A Posteriori (MAP)scheme (MAP) scheme or a Register Exchange Soft Output

Viterbi Algorithm (RESOVA) scheme instead of the SISO scheme is a

scheme for calculating probability with reliability for a symbol, and the RESOVA scheme is a

scheme for calculating probability for a codeword by considering a path over which a symbol

passes as a long codeword.

Please insert a space between the paragraph beginning at page 6, line 7, and ending at page 6,

line 16, and the paragraph beginning at page 6, line 17 and ending at page 6, line 22.

Please insert a space between the paragraph beginning at page 26, line 20, and ending at page 27,

line 9, and the paragraph beginning at page 27, line 10 and ending at page 27, line 22.

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